

REMARKS

This is a full and timely response to the outstanding non-final Office Action mailed October 10, 2002. Reconsideration and allowance of the application and presently pending claims, as amended, are respectfully requested.

I- Present Status of Patent Application

Upon entry of the amendments in this response, claims 1-18 remain pending in the present application, with claims 1-3, 8-10, and 13-18 being amended. It is believed that the foregoing amendments add no new matter to the present application.

II- Independent Claim 1

Claim 1 stands rejected under 35 U.S.C. 103(a) as being unpatentable over U.S. Patent No. 6, 177, 288 (*Takiar*) in view of Japanese Patent No. 09066429 (*Kitamura*). Applicants maintain that *Takiar* in view of *Kitamura* does not disclose, teach, or suggest at least the step of "creating a plurality of holes in a substrate, wherein each of the plurality of holes corresponds to one of the plurality of lcnds." Therefore, applicants respectfully assert that *Takiar* in view of *Kitamura* does not render claim 1 obvious, and that the rejection should be withdrawn.

III- Dependent claims 2-8

Since independent claim 1 is allowable over the prior references of record, then its dependent claims 2-8 are allowable as a matter of law, because each of these claims includes all the steps of independent claim 1. *In re Fine*, 837 F.2d 1071 (Fed. Cir. 1988).

IV- Independent Claim 9

Claim 9 stands rejected under 35 U.S.C. 103(a) as being unpatentable over *Takiar* in view of *Kitamura*. Applicants maintain that *Takiar* in view of *Kitamura* does not disclose, teach, or suggest at least the step of "testing said plurality of lcnds while they are connected to each other and to a connection for conducting a test signal." Therefore, applicants respectfully assert that *Takiar* in view of *Kitamura* does not render claim 9 obvious, and that the rejection should be withdrawn.

V- Dependent claims 10-13

Since independent claim 9 is allowable over the prior references of record, then its dependent claims 10-13 are allowable as a matter of law, because each of these claims includes all the steps of independent claim 9.

VI- Independent Claim 14

Claim 14 stands rejected under 35 U.S.C. 103(a) as being unpatentable over *Takiar* in view of *Kitamura*. Applicants maintain that *Takiar* in view of *Kitamura* does not disclose, teach, or suggest at least "a second substrate having a plurality of sealed holes extending through a thickness thereof, wherein each of the plurality of sealed holes corresponds to one of a plurality of lcnds." Therefore, applicants respectfully assert that *Takiar* in view of *Kitamura* does not render claim 14 obvious, and that the rejection should be withdrawn.

VII- Dependent claims 15-18

Since independent claim 14 is allowable over the prior references of record, then its dependent claims 15-18 are allowable as a matter of law, because each of these claims includes all the elements of independent claim 14.

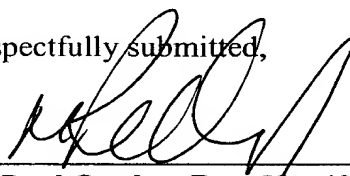
VIII- Amended Specification

Applicants have amended the specification as suggested by the Examiner.

CONCLUSION

In light of the foregoing amendments and for at least the reasons set forth above, Applicants respectfully submit that all rejections have been traversed, rendered moot, and/or accommodated, and that the now pending claims 1-18 are in condition for allowance. Favorable reconsideration and allowance of the present application and all pending claims are hereby courteously requested. If, in the opinion of the Examiner, a telephone conference would expedite the examination of this matter, the Examiner is invited to call the undersigned attorney at (770) 933-9500.

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'M. Paul Qualey', is written over a horizontal line.

M. Paul Qualey, Reg. No. 43,024

**ANNOTATED VERSION OF MODIFIED CLAIMS TO SHOW CHANGES
MADE**

The following is a marked up version of the amended claims. Amend the following claims by adding the language that is underlined (“ ”) and by deleting the language that is enclosed within brackets (“[]”):

1. (Once Amended) A method of manufacturing a plurality of liquid crystal micro displays (lcmds), said method comprising:
creating [a hole] a plurality of holes in a substrate, wherein each of the plurality of holes corresponds to one of the plurality of lcmds;
causing liquid crystal material to flow through said [hole] plurality of holes, and to fill spaces within said plurality of lcmds; and
sealing said [hole] plurality of holes.
2. (Once Amended) The method of claim 1, further comprising :
testing said [lcmd] plurality of lcmds after sealing said [hole] plurality of holes.
3. (Once Amended) The method of claim 2, further comprising:
separating said [lcmd] plurality of lcmds from each other [lcmds] after
testing said [lcmd] plurality of lcmds.
8. (Once Amended) The method of claim 1, wherein said [hole is] plurality of holes are sealed using a sealant material selected from a group consisting of glue, epoxy, and solder.
9. (Once Amended). A method of manufacturing a [first] plurality of liquid crystal micro displays (lcmds) comprising:
[testing said first lcmd while it is physically connected to a second lcmd; and]
testing said plurality of lcmds while they are connected to each other and to a connection for conducting a test signal; and
[separating said first lcmd from said second lcmd after said testing.]
separating said plurality of lcmds from each other after said testing.

10. (Once Amended) The method of claim 9, wherein [said first lcdm] each of said plurality of lcdms comprises a semiconductor substrate having an integrated circuit and a glass substrate having a transparent electrode.
13. (Once Amended) The method of claim 12, wherein said testing includes determining whether [the lcdm produces a uniform image] each of the plurality of lcdms produces a uniform image.
14. (Once Amended) A liquid crystal micro display (lcdm) assembly comprising:
a first substrate; [and]
a second substrate having [a hole] a plurality of sealed holes extending through a thickness thereof [.] wherein each of the plurality of sealed holes corresponds to one of a plurality of lcdms; and
liquid crystal material that is located between the first substrate and the second substrate, and within the plurality of lcdms.
15. (Once Amended) The lcdm assembly of claim 14, wherein said plurality of sealed holes were used for filling the plurality of lcdms with liquid crystal material prior to the plurality of sealed holes being sealed. [hole can be used for filling the lcdm with liquid crystal material.]
16. (Once Amended) The lcdm assembly of claim 14, wherein the second substrate is a semiconductor substrate comprising an integrated circuit.
17. (Once Amended) The lcdm assembly of claim 14, wherein the second substrate comprises glass.
18. (Once Amended) The lcdm assembly of claim 14, [wherein said lcdm is physically connected to other lcdms.] wherein each of the plurality of lcdms comprises a portion of the first substrate, a portion of the second substrate, and a portion of the liquid crystal material.

**ANNOTATED VERSION OF MODIFIED SPECIFICATION TO SHOW
CHANGES MADE**

Changes to Page 4, line 9:

FIG. 1 depicts an example of a top view of a prior art lcd substrate assembly.